November 24, 2026

National Stock Exchange of India Limited
Exchange Plaza
Bandra-Kurla Complex, Bandra (E)
Mumbai-400051
Symbol-ACI

BSE Limited
Listing Operations
P J Towers Dalal Street
Mumbai-400001
Scrip Code- 543657

Dear Sir/Madam,

Subject: Appointment of Chief Executive Officer of SiCSem Private Limited, a step-down subsidiary of the Company

Reference: Regulation 30 read with Part A of Schedule III of the SEBI (Listing Obligations and Disclosure Requirements) Regulations, 2015 ("SEBI LODR")

Pursuant to Regulation 30 of SEBI LODR, we wish to inform you that Mr. Rajesh Nair has been appointed as Chief Executive Officer of SiCSem Private Limited ("SiCSem"), a step-down subsidiary of the Company, with effect from November 09, 2026.

SiCSem is setting up a Silicon Carbide power semiconductor fabrication and ATMP facility at Info Valley, Khordha, Odisha, under the India Semiconductor Mission. As Chief Executive Officer, Mr. Nair will lead SiCSem through the construction, commissioning and commercial operation of the facility.

The requisite details, as per Regulation 30 read with Schedule III of the Listing Regulations and SEBI Circular HO/49/14/14(7)2025-CFD-POD2/I/3762/2026 dated January 30, 2026, are attached with this intimation as Annexure-I.

The above information shall be made available on the website of the Company at www.archeanchemicals.com

Yours faithfully
For Archean Chemical Industries Limited

NEMAM ECHAMPADI VIJAYARAGHAVAN
Digitally signed by NEMAM ECHAMPADI VIJAYARAGHAVAN
Date: 2026.11.24 21:51:24 +05'30'

Vijayaraghavan NE
Company Secretary and Compliance Officer
M. No. A 41671

Encl: Annexure A

Annexure A

Sr.No.	Particulars	Details
1	Name	Mr. Rajesh Nair
2	Reason for change, viz. appointment, re-appointment, resignation, removal, death or otherwise	Appointment as Chief Executive Officer of SiCSem Private Limited, a step-down subsidiary of the Company.
3	Date & term of appointment	Appointed with effect from November 09, 2026.
4	Brief profile (in case of appointment)	Mr. Rajesh Nair has over 30 years of executive leadership experience across semiconductor business units, technology R&D and factory operations. Prior to joining SiCSem, he served as Chief Technology Officer and Senior Vice President for Process Integration and R&D at Tata Electronics, where he was one of the senior executives responsible for setting up India's first 300mm wafer fab. Before Tata Electronics, he spent eleven years at GlobalFoundries, in Singapore and the US in Technology Development and Business Unit roles. Raj was Vice President and General Manager, Analog/Power and Embedded NVM, a USD 2 billion revenue business. Earlier in his career, he held senior fab operations, technology development and engineering leadership roles at Johnson Controls, ON Semiconductor and Motorola. Raj has previously served on the Advisory Board of the India Semiconductor Mission and holds seven US patents. Mr. Nair holds a B. Tech in Chemical Engineering from the Indian Institute of Technology Bombay, an M.S. in Chemical Engineering from Syracuse University, and an M.S. in Electrical Engineering from National Technological University, USA.
5	Disclosure of relationships between directors (in case of appointment of a director)	Not applicable. Mr. Rajesh Nair is not related to any Director or Key Managerial Personnel of the Company.